

**IN THE TITLE:**

Please amend the Title as follows:

“PROCESS FOR CREATING PHASE EDGE STRUCTURES IN A PHASE SHIFT  
MASK”

**IN THE SPECIFICATION:**

Please amend paragraph [0019] as follows:

[0019] In FIGS. 2A-2B, which again are top and cross-sectional views (drawn along line X-X'), a mask 104 is patterned to protect one of the openings 100. The other opening 102 is then etched to create a recess in the transparent substrate 110. The mask is subsequently removed, as shown in FIGS. 3A-3B, and the separating portion of opaque material 106 is also removed (using a number of different methods such as additional masking and etching, etc.). This produces a larger opening (openings 100[[,] and 102 combined) that has a phase shift feature where light passing through portion 100 is shifted 180 degrees from the light passing through portion 102.

Please amend paragraph [0020] as follows:

[0020] This process shown in Figures 1A-3B involves a number of masking, patterning, photolithographic, etching, etc. techniques. The invention shown in Figures 4A-6B substantially simplifies the process to produce the same structure. As with previous drawings, Figures 4A-6A are top views and Figures 4B-6B are cross-sectional views drawn along line X-X'. As shown in Figures 4A and 4B, the invention performs a first patterning of the opaque layer 112 to expose a first region 114 of the transparent substrate 110. The invention then etches the first region 114 of the transparent substrate 110 through the opaque layer 112 to create a phase shift region within the transparent substrate 110, as shown in Figures 4A and 4B. Next, the invention performs additional



the overlay requirements for each level also become less stringent, which improves yield, and TAT.

Please amend paragraph [0025] as follows:

[0025] Overlay requirements are relaxed with the invention because in the original method (illustrated in Figures 1A to 3B), the second level lithography process had to land on the opaque region (106) between the two clear openings (~~442~~ 100 and 102, as shown in Figure 2B). However, in the second process of the embodiments of the invention (illustrated in Figures 4A to 5B), the second lithography process only has to hit the large opening (114 and 116 as shown in Figure 5B, which provides more room for error).